

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Carles Borrego Bel et al.

Group Art Unit: 2836

Examiner: Adi Amrany

Serial No.: 10/709,677

Filed: May 21, 2004

For: SYSTEM AND METHOD FOR PROTECTING AGAINST
SHORT CIRCUITS IN ELECTRIC POWER DISTRIBUTION
ARCHITECTURES WITH TWO VOLTAGE LEVELS

Attorney Docket No.: LEAR 8153ES PUSA

**INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. § 1.97(b)**

Mail Stop Amendment
Commissioner for Patents
U.S. Patent & Trademark Office
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In compliance with the duty of disclosure under 37 C.F.R. § 1.56 and §§ 1.97-1.98, the reference listed and identified on the attached Form PTO/SB08A is being submitted herewith for consideration by the Examiner. This statement is being filed in accordance with 37 C.F.R. § 1.97(b). EP 1332974 as provided herewith is believed to be an equivalent of Spanish Patent Application P200003143 as cited in the Information Disclosure Statement filed on May 6, 2008. An English translation of P200003143 was not found at the time the Information Disclosure Statement was filed on May 6, 2008. Applicants have since found an English translation and submit EP 1332924 for consideration by the Examiner.

While this Statement is being filed in compliance with the duty of disclosure, citation of the listed reference is not to be construed as an admission that the reference is "material" as defined under 37 C.F.R. § 1.56(b). Consideration and entry into the record of the listed reference is respectfully requested.

Respectfully submitted,
Carles Borrego Bel et al.

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Date: May 15, 2008

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